9. (Amended) The storage device according to claim 1, wherein the circuit inputs part of the address from the data input to set the part to selected bit of a held address that is modified by the part, and provides the modified address to the memory.

19. (Amended) A method for access to a storage device having a data input, a control input, a data output, and a memory for storing data, wherein the storage device provides an address from the data input to the memory in accordance with a control signal from the control input to store data provided to the data input at the provided address in the memory or to output data at the address in the memory to the data output, comprising:

    providing an address to the data input to output the address to the memory;

    providing data to the data input to output the data to the memory; and

    providing a first control signal to the control input to store the data at the address into the memory.

22. (Amended) The method according to claim 19, further comprising:

    providing an address to the data input to set the address of memory; and

    providing a second control signal to the control input to output the data at the address in the memory to the data output.

23. (Amended) The method according to claim 19, further comprising, providing part of the address to set the part to selected bits of an address held in the storage device for modifying the address and output the modified address to the memory.

28. (Amended) An apparatus having (1) an interface for data exchange from and to a storage device having a data input, a control input, a data output, and a memory for storing data, wherein the storage device provides an address from the data input to the memory in accordance with a control signal from the control input to store data provided to the data input at the provided address in the memory or to output data at the address in the memory to the data output, and (2) a connector for connecting the data input, a data output and the control input to the interface and supplying power to the storage device, wherein the apparatus provides the control signal to the control input from the interface, and the data or an address to the data input from the interface to store the data at the address in the memory or to output data at the address in the

memory to the data output.

31. (Amended) The apparatus according to claim 28, wherein the apparatus provides part of the address to the data input to set the part to selected bits of an address held in the storage device for modifying the address and output the modified address to the memory.

Please add the following claims 39-42:

39. (New) An apparatus having (1) an interface for data exchange from and to the external storage device having a data input, a data output, a control input, and a memory provided an address from the data input in accordance with a control signal from the control input to output data at the provided address in the memory from the data output, and (2) a connector for connecting the data input, a data output and the control input to the interface and supplying power to the external storage device, wherein the apparatus provides the control signal to the control input, and changed part of the address to the data input from the interface to set the part to selected bits of the address held in the external storage device for modifying the address so that the external storage device outputs the data stored at the modified address in the memory from the data output.

40. (New) An apparatus having (1) an interface for data exchange from and to the external storage device having a data input, a data output, a control input, and a memory provided an address from the data input in accordance with a control signal from the control input to output data at the provided address in the memory from the data output, wherein the memory has a mode in which the data are successively output from the memory, and (2) a connector for connecting the data input and the control input to the interface and supplying power to the external storage device, wherein the apparatus provides the control signal to the control input, and the address to the data input from the interface to output data at the address in the memory from the data output, and sets a value into the interface to turn control status of the memory such that the memory operates in the mode.

41. (New) An apparatus having (1) an interface for data exchange from and to the external storage device having a data input, a data output, a control input, and a memory provided an address from the data input in accordance with a control signal